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SCHEDULE

Part A
Hardware Binaries:
FPGA and PLD configuration bitstream files for any or all of the Hardware Source identified below in this Part A

Software Binaries:
Boot monitor binary
Keil USB and SD card drivers
Lattice ispClock output files in binary
SelfTest
Motherboard configuration controller
Daughterboard configuration controller

Documentation:
Documentation, provided as PDF

Hardware Source
Hardware netlists of the ARM PrimeCell peripheral technology and components known as BP130, BP132, PL111, PL301, PL330 and PL354.

Part B
Wrapper:
Wrapper file provided as hardware source files and netlists

Board Reference Designs:
Board reference design data for HPI-0232 (V2C-002) and HPI-212 (V2T-A7), including Gerber files, OrCad and PADS databases.

Part C: Example Code

(i)   Platform initialisation source code
         Platform specific libraries and source code

(ii)   ARM source code portions of the boot monitor and SelfTest

Part D: Separate Files

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B.   The following are all licensed under the GNU General Public License, Version 2:

         Linux kernel binary image and corresponding source files for such image
         self test smsc9118 pseudo code

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